

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	27	714/719.ccls. and decod\$ and MUX	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 11:03
S2	27	714/719.ccls. and decod\$ and MUX and select	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 11:04
S3	24	714/719.ccls. and decod\$ and MUX and select and compare	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 13:24
S4	318032	(compared comparison comparing compare) same (memory store storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 13:25
S5	75535	(compared comparison comparing compare) same (memory store storage) same result	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 13:25
S6	44980	(result with (compared comparison comparing compare)) same (memory store storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 13:26
S7	25182	(result with (compared comparison comparing compare)) with (memory store storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 13:26
S8	1464	(result with (compared comparison comparing compare)) with (memory store storage) and "714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 13:27
S9	127	(result with (compared comparison comparing compare)) with (memory store storage) and 714/719.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:16
S10	99	(result with (compared comparison comparing compare)) with (memory store storage) and 714/719.ccls.	USPAT	OR	ON	2004/08/16 13:27

S11	227474	memory and (test testing tested tester)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 16:39
S12	147147	memory and (test testing tested tester) and (compare comparing comparison compared)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 11:32
S13	122722	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:45
S14	983	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and (714/718.ccls. 714/719.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 11:34
S15	455	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and (714/718.ccls. 714/719.ccls.) and (hold holding held)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 11:41
S16	127	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and (714/718.ccls. 714/719.ccls.) and ((hold holding held) same(compare comparing comparison compared))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:30
S17	192	(britt harris).xa. and compare	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:31
S18	34	(britt harris).xa. and compare and "714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:38
S19	30	(britt harris).xa. and memory and parallel and "714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:44

S20	1123	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 17:41
S21	258	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and ((compare comparing comparison compared) with (write expected original))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:47
S22	296	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and ((compare comparing comparison compared) with (write written expected original))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:47
S23	277	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and ((compare comparing comparison compared) with (write written expected original)) and (processor processing microprocessor CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/09 13:49
S24	277	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each another other)) and ((compare comparing comparison compared) with (write written expected original)) and (processor processing microprocessor CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 13:06
S25	34442	parallel with (test testing tester tested)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 13:06

S26	3160	parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 13:08
S27	1860	parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor IC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 13:35
S28	572	parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor IC) and ((test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor IC)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 13:35
S29	1253	parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC) and ((test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 13:36
S30	502	parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC) and ((test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC)).ab. and ((compare comparing comparison compared) with (output result))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 16:42
S31	51	parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC) and ((test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC)).ab. and ((compare comparing comparison compared) with (output result)) and 714/719. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 16:41

S32	100	( "6076136" "5047972" "4453209" "5512766" "5255383" "5561671" "5959692" "6237122" "5388074" "5717653" "5729500" "5732278" "5838604" "6088291" "6246620" "5319395" "5802587" "5818789" "6163832" "4327291" "4384348" "4397000" "4562488" "4628497" "4864543" "4954991" "4964046" "5231673" "5245585" "5313551" "5365485" "5497473" "5506806" "5544341" "5586282" "6055615" "4319084" "4387308" "4424587" "4443845" "4458332" "4532549" "4572974" "4580039" "4623990" "4893298" "4914575" "4942593" "4945518" "4959822").pn. ("5237670" "5287327" "5313594" "5339276" "5430688" "5438559" "5444858" "5477494" "5500848" "5502488" "5519498" "5521879" "5535353" "5550996" "5577229" "5579267" "5586299" "5592348" "5596731" "5615190" "5715410" "5757809" "5818771" "5829015" "5838895" "5841727" "5845314" "5886708" "5912849" "5959935" "6011728" "6029211" "6118742" "6181609" "6191992" "6341192" "4242973" "4247921" "4270190" "4278838" "4322752" "4323980" "4325135" "4326282" "4335405" "4366535" "4373168" "4387449" "4392198" "4397008").pn.	USPAT	OR	ON	2004/08/10 13:52
S33	50	( "6173425" "4975876" "5193092" "5561671" "5961653" "4320881" "4429388" "4493073" "4809231" "4903265" "4951254" "5255271" "5331644" "5424984" "5436910" "5633877" "5691951" "5691950" "5818772" "5854765" "5898704" "5920573" "5968194" "5982681" "6108252" "6237122" "6243841" "4268911" "4287594" "4297680" "4312067" "4339801" "4409635" RE31582 "4482953" "4504929" RE31864 "4510580" "4519077" "4574068" "4580230" "4601032" "4622652" "4622647" "4626769" "4628443" "4768194" "4862418" "4872168" "4897817").pn.	USPAT	OR	ON	2004/08/10 16:41

S34	51	parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC) and ((test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC)).ab. and ((compare comparing comparison compared) with (output result)) and 714/719. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 16:41
S35	50	("6173425" "4975876" "5193092" "5561671" "5961653" "4320881" "4429388" "4493073" "4809231" "4903265" "4951254" "5255271" "5331644" "5424984" "5436910" "5633877" "5691951" "5691950" "5818772" "5854765" "5898704" "5920573" "5968194" "5982681" "6108252" "6237122" "6243841" "4268911" "4287594" "4297680" "4312067" "4339801" "4409635" RE31582 "4482953" "4504929" RE31864 "4510580" "4519077" "4574068" "4580230" "4601032" "4622652" "4622647" "4626769" "4628443" "4768194" "4862418" "4872168" "4897817").pn.	USPAT	OR	ON	2004/08/10 16:42
S36	0	(parallel with (test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC) and ((test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC)).ab. and ((compare comparing comparison compared) with (output result)) and 714/719. ccls. ) and ((("6173425" "4975876" "5193092" "5561671" "5961653" "4320881" "4429388" "4493073" "4809231" "4903265" "4951254" "5255271" "5331644" "5424984" "5436910" "5633877" "5691951" "5691950" "5818772" "5854765" "5898704" "5920573" "5968194" "5982681" "6108252" "6237122" "6243841" "4268911" "4287594" "4297680" "4312067" "4339801" "4409635" RE31582 "4482953" "4504929" RE31864 "4510580" "4519077" "4574068" "4580230" "4601032" "4622652" "4622647" "4626769" "4628443" "4768194" "4862418" "4872168" "4897817").pn. )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 16:42

S37	10	(( "6173425" "4975876" "5193092" "5561671" "5961653" "4320881" "4429388" "4493073" "4809231" "4903265" "4951254" "5255271" "5331644" "5424984" "5436910" "5633877" "5691951" "5691950" "5818772" "5854765" "5898704" "5920573" "5968194" "5982681" "6108252" "6237122" "6243841" "4268911" "4287594" "4297680" "4312067" "4339801" "4409635" RE31582 "4482953" "4504929" RE31864 "4510580" "4519077" "4574068" "4580230" "4601032" "4622652" "4622647" "4626769" "4628443" "4768194" "4862418" "4872168" "4897817").pn. ) and (semiconductor chip circuit IC) and ((test testing tester tested) same (memory RAM ROM SRAM DRAM PROM) and (semiconductor chip circuit IC)).ab. and ((compare comparing comparison compared) with (output result))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/10 16:42
S38	12	(britt and james).in.	USOCR	OR	ON	2004/08/11 14:24
S39	104	(britt).in.	USOCR	OR	ON	2004/08/11 14:28
S40	30320	(britt).in. hair	USOCR	OR	ON	2004/08/11 14:25
S41	2	(britt).in. and hair	USOCR	OR	ON	2004/08/11 14:26
S42	414297	(britt).in. and new york	USOCR	OR	ON	2004/08/11 14:28
S43	13	(britt).in. and (new adj york)	USOCR	OR	ON	2004/08/11 14:29
S44	345	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and decod\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 17:41
S45	154	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and decod\$ and (multiplexer multiplexor MUX)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:17
S46	230	714/719.ccor.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 17:44

S47	119	714/719.ccor. and decod\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 17:44
S48	17	714/719.ccor. and decod\$ and MUX	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 11:03
S49	2	"5475695".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/13 18:42
S50	41	"5475695".URPN.	USPAT	OR	ON	2004/08/13 18:52
S51	6	"5475695".URPN. and signature	USPAT	OR	ON	2004/08/13 18:52
S62	224719	(compar\$ near2 output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:08
S65	282	(compar\$ near2 output) and (memory adj test\$4).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:05
S66	135	(compar\$ near2 output) and (memory adj test\$4).ab. and "714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:05
S68	1888	(compar\$ with result) and (memory adj test\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:09
S69	12918	(compar\$ with output) and (memory with test\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:06
S70	7661	S62 and S69	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:08

S71	819	S62 and S69 and S68	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:08
S72	89	S62 and S69 and S68 and S66	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:42
S73	360	(compar\$ with result) and (memory adj test\$4).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:09
S74	130	(result with (compared comparison comparing compare)) with (memory store storage) and 714/719.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:16
S75	185	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and decod\$ and (multiplexer multiplexor MUX)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:17
S76	4	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and decod\$ and (multiplexer multiplexor MUX) and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:18
S77	0	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and decod\$ and (multiplexer multiplexor MUX) and S72	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:18

S78	2	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and decod\$ and (multiplexer multiplexor MUX switch select selector) and S72	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:18
S79	6	memory and (test testing tested tester) and (compare comparing comparison compared) and (select selection selector selecting selected) and ((compare comparing comparison compared) with (each other)) and decod\$ and (multiplexer multiplexor MUX switch select selector) and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 11:18
S80	32	S62 and S69 and S68 and S66 and (parallel with test\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 14:07
S81	2	"20020188900"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 14:07